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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shunpei Yamazaki et al. Art Unit : 2811
Serial No. : 10/718,584 Examiner : Unknown
Filed : November 24, 2003
Title : SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants submit the reference listed on the attached form PTO-1449.

This statement is being filed before the receipt of a first Office action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 1, 2004


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Substitute Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office		Attorney's Docket No. 12732-092002	Application No. 10/718,584
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))				Applicant Shunpei Yamazaki et al.	
				Filing Date November 24, 2003	Group Art Unit 2811

U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	5,990,998	11/23/1999	Park et al.			04/10/1997
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications							
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						

Other Documents (include Author, Title, Date, and Place of Publication)		
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	AR	
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Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	